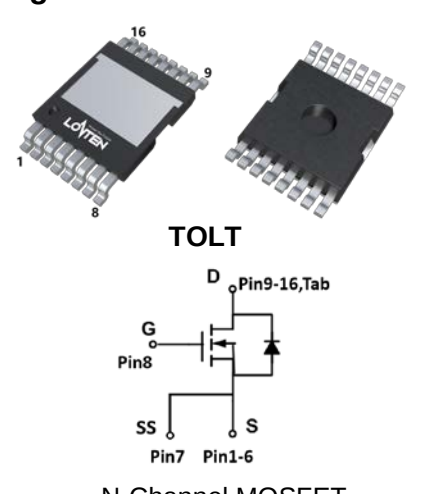


Lonten N-channel 650V, 40A, 99mΩ LonFET™ Power MOSFET

Description LonFET™ Power MOSFET is fabricated using advanced super junction technology. The resulting device has extremely low on resistance, making it especially suitable for applications which require superior power density and outstanding efficiency. Features ● Ultra low RDS(on) ● Ultra low gate charge (typ. Qg = 70.5nC) ● 100% UIS tested ● RoHS compliant Applications ● Power factor correction (PFC). ● Switched mode power supplies (SMPS). ● Uninterruptible power supply (UPS).	Product Summary <table> <tr> <td>V_{DS}</td><td>650V</td></tr> <tr> <td>R_{DS(on),max}</td><td>99mΩ</td></tr> <tr> <td>I_{DM}</td><td>120A</td></tr> <tr> <td>Q_{g,typ}</td><td>70.5nC</td></tr> </table> Pin Configuration 	V _{DS}	650V	R _{DS(on),max}	99mΩ	I _{DM}	120A	Q _{g,typ}	70.5nC
V _{DS}	650V								
R _{DS(on),max}	99mΩ								
I _{DM}	120A								
Q _{g,typ}	70.5nC								

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	650	V
Continuous drain current (T _C = 25°C)	I _D	40	A
(T _C = 100°C)		25.3	A
Pulsed drain current ¹⁾	I _{DM}	120	A
Gate-Source voltage	V _{GSS}	±30	V
Avalanche energy, single pulse ²⁾	E _{AS}	992	mJ
Power Dissipation	P _D	344	W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C
Continuous diode forward current	I _S	40	A
Diode pulse current	I _{S,pulse}	120	A

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R _{θJC}	0.36	°C/W
Thermal Resistance, Junction-to-Ambient ³⁾	R _{θJA}	62	°C/W
Soldering temperature, wave soldering only allowed at leads. (1.6mm from case for 10s)	T _{sold}	260	°C

Package Marking and Ordering Information

Device	Device Package	Marking	Units/Reel
LSTT65R099GF	TOLT	LSTT65R099GF	1200

Electrical Characteristics

 $T_c = 25^{\circ}\text{C}$ unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static characteristics						
Drain-source breakdown voltage	BV _{DSS}	V _{GS} =0 V, I _D =0.25mA	650	-	-	V
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =0.25mA	2.5	3.0	5.0	V
Drain cut-off current	I _{DSS}	V _{DS} =650 V, V _{GS} =0 V, T _j = 25°C	-	-	5	μA
Gate leakage current, Forward	I _{GSSF}	V _{GS} =30 V, V _{DS} =0 V	-	-	100	nA
Gate leakage current, Reverse	I _{GSSR}	V _{GS} =-30 V, V _{DS} =0 V	-	-	-100	nA
Drain-source on-state resistance	R _{DS(on)}	V _{GS} =10 V, I _D =20 A	-			mΩ
		T _j = 25°C	-	92	99	
		T _j = 150°C	-	200	-	
Gate resistance	R _G	f=1 MHz, open drain	-	1.9	-	Ω
Dynamic characteristics						
Input capacitance	C _{iss}	V _{DS} = 100 V, V _{GS} = 0 V, f = 250kHz	-	3580	-	pF
Output capacitance	C _{oss}		-	110	-	
Reverse transfer capacitance	C _{rss}		-	3.2	-	
Turn-on delay time	t _{d(on)}	V _{DD} = 400V, I _D = 20A R _G = 10Ω, V _{GS} =10V	-	51.4	-	ns
Rise time	t _r		-	45.6	-	
Turn-off delay time	t _{d(off)}		-	106	-	
Fall time	t _f		-	5.5	-	
Gate charge characteristics						
Gate to source charge	Q _{gs}	V _{DD} =520V, I _D =20A, V _{GS} =0 to 10 V	-	18.9	-	nC
Gate to drain charge	Q _{gd}		-	25.1	-	
Gate charge total	Q _g		-	70.5	-	
Gate plateau voltage	V _{plateau}		-	6	-	V
Reverse diode characteristics						
Diode forward voltage	V _{SD}	V _{GS} =0 V, I _F =40A	-	-	1.1	V
Reverse recovery time	t _{rr}	V _R =400V, I _F =20A, dI _F /dt=100 A/μs	-	170.1	-	ns
Reverse recovery charge	Q _{rr}		-	1.3	-	μC
Peak reverse recovery current	I _{rrm}		-	15.4	-	A

Notes:

- Limited by maximum junction temperature, maximum duty cycle is 0.75.
- $I_{AS} = 5.75\text{A}$, $L=60\text{mH}$, $V_{DD} = 60\text{V}$, Starting $T_j = 25^{\circ}\text{C}$.
- The value of R_{thJA} is measured by placing the device in a still air box which is one cubic foot.

Electrical Characteristics Diagrams

Figure 1. Typ. Output Characteristics

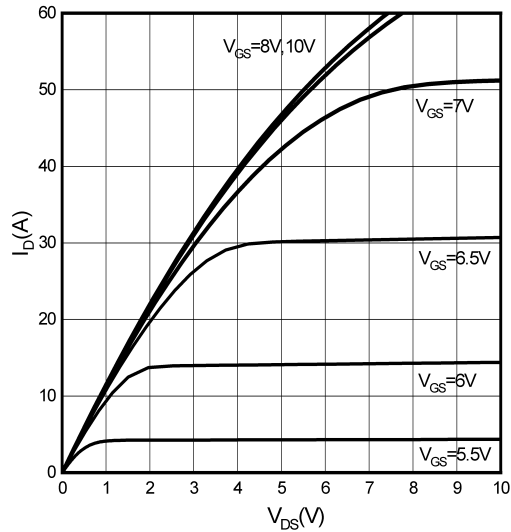


Figure 2. Transfer Characteristics

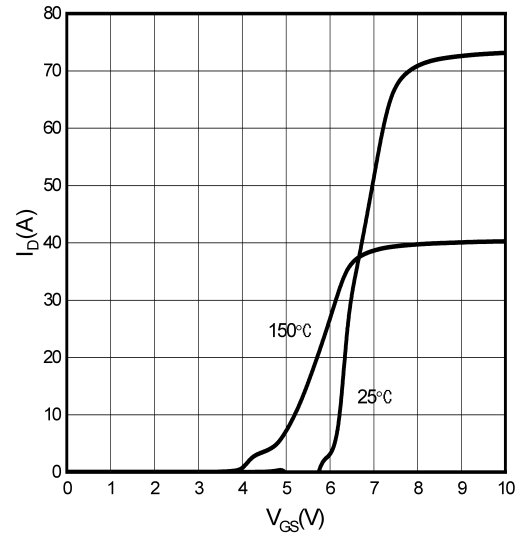


Figure 3. On-Resistance vs. Drain Current

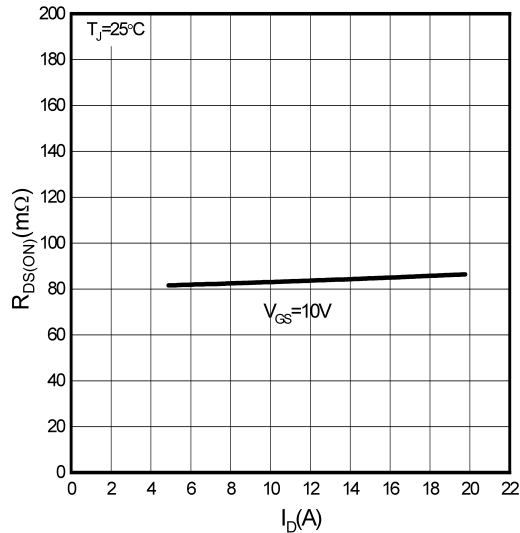


Figure 4. On-Resistance vs. Temperature

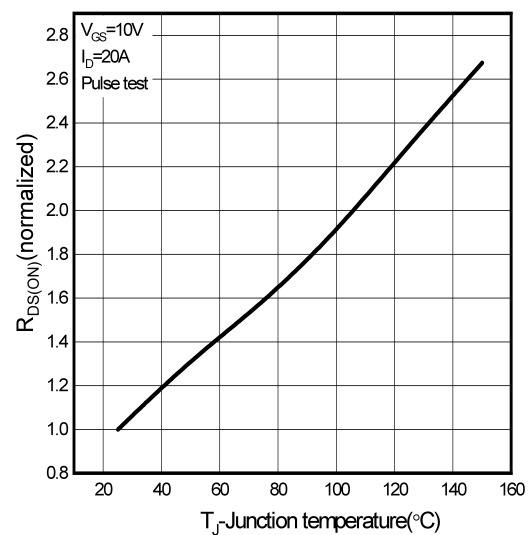


Figure 5. Breakdown Voltage vs. Temperature

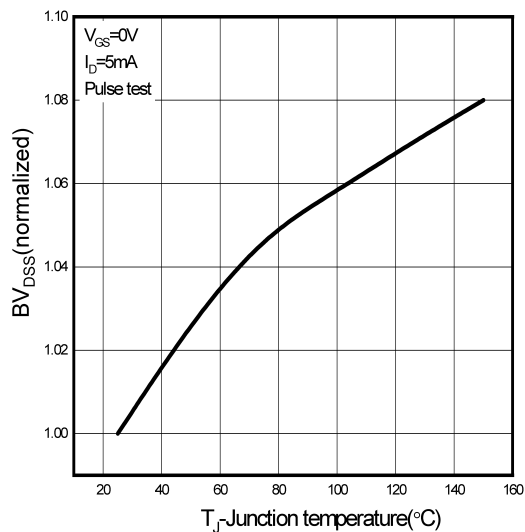


Figure 6. Threshold Voltage vs. Temperature

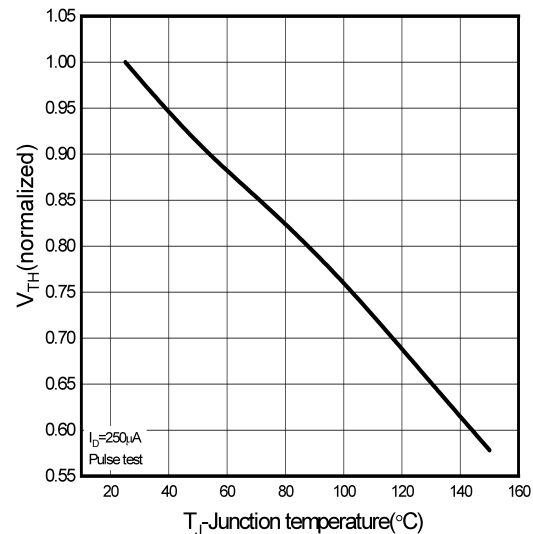


Figure 7. Body-Diode Characteristics

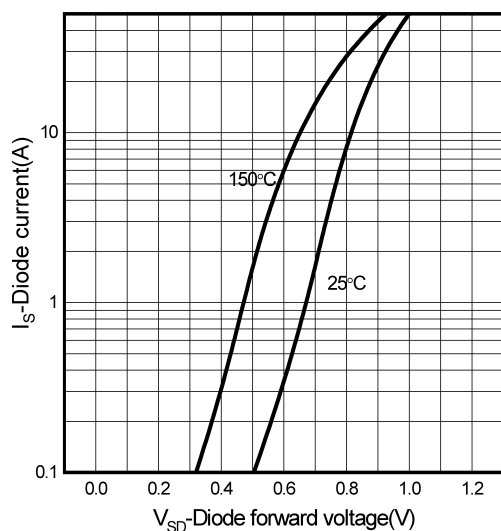


Figure 8. Capacitance Characteristics

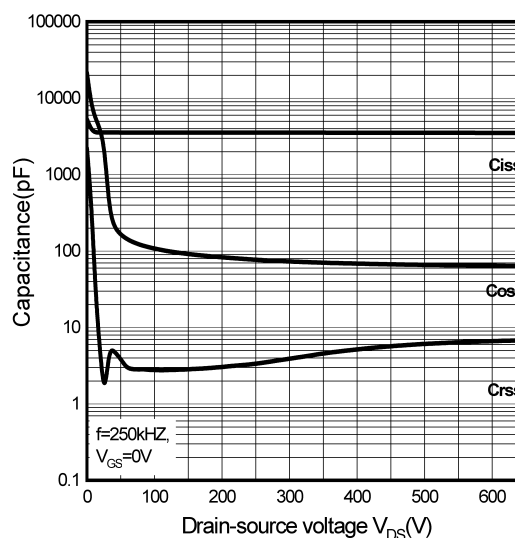


Figure 9. Gate Charge Characteristics

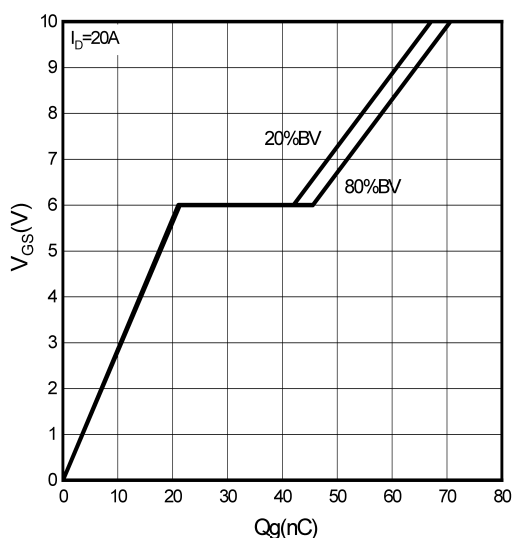


Figure 10. Drain Current Derating

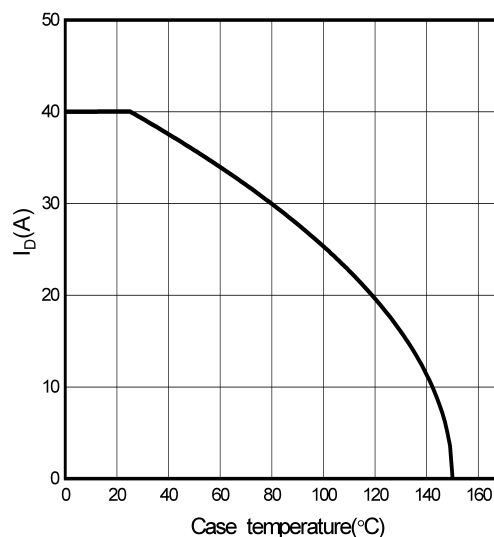


Figure 11. Power Dissipation vs. Temperature

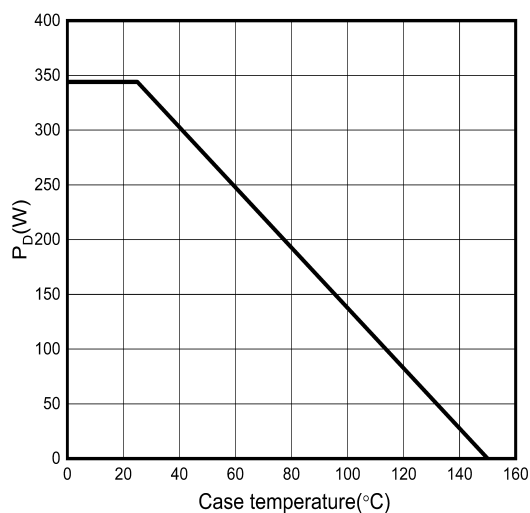


Figure 12. Safe Operating Area

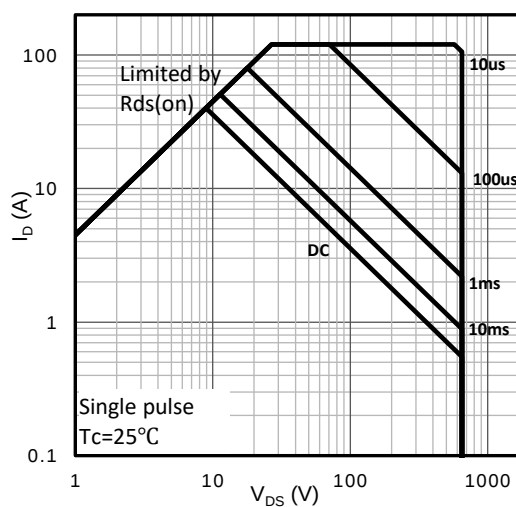
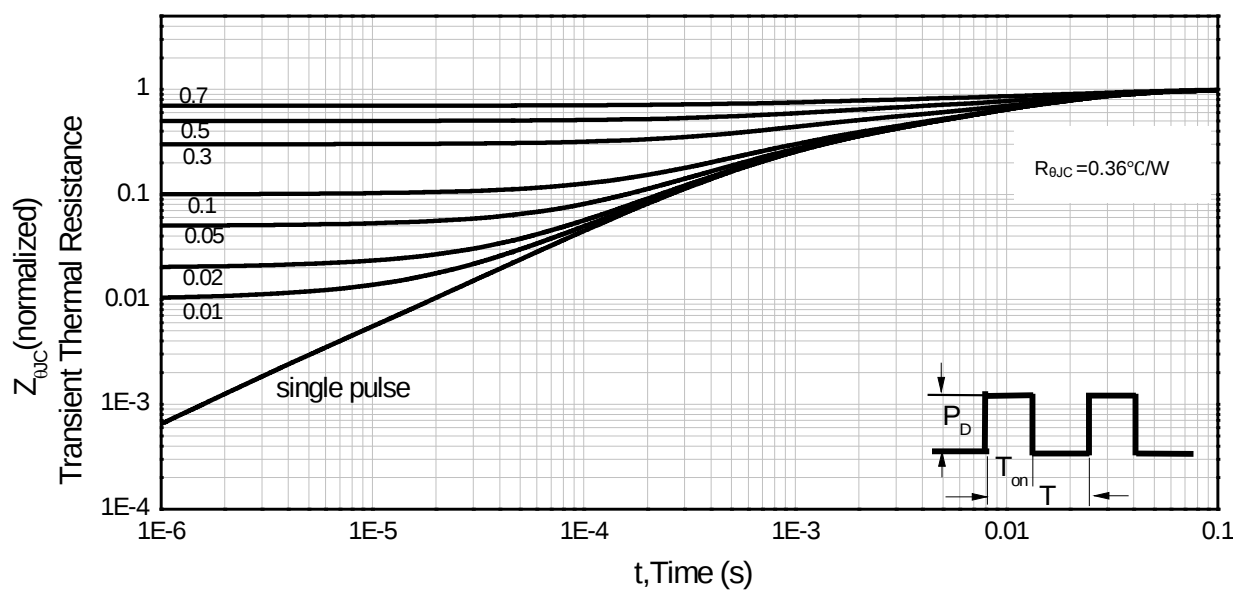
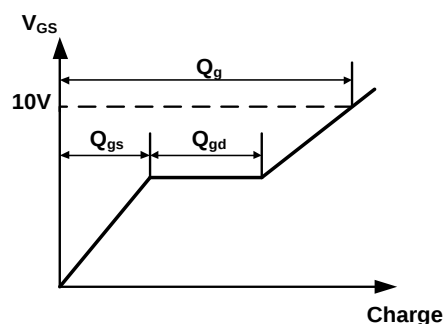
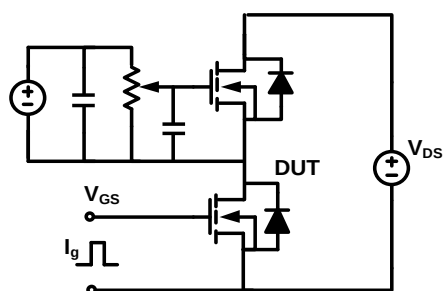


Figure 13. Normalized Maximum Transient Thermal Impedance (RthJC)

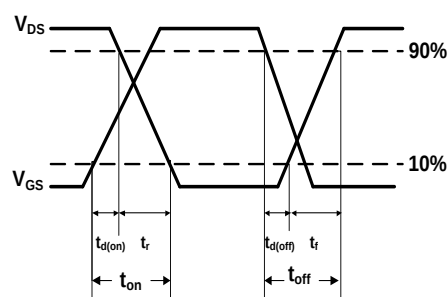
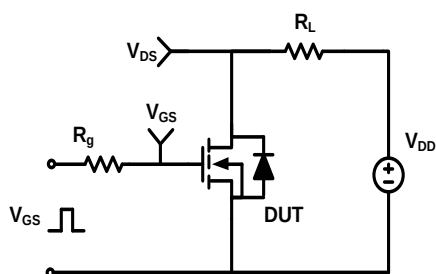


Test Circuit & Waveforms

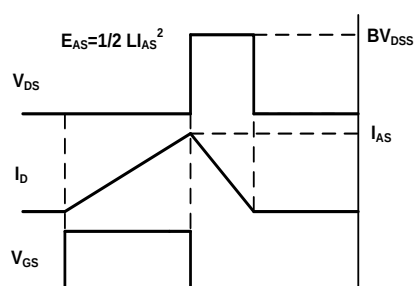
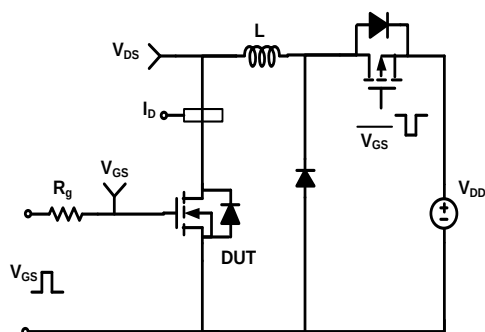
Gate Charge Test Circuit & Waveform



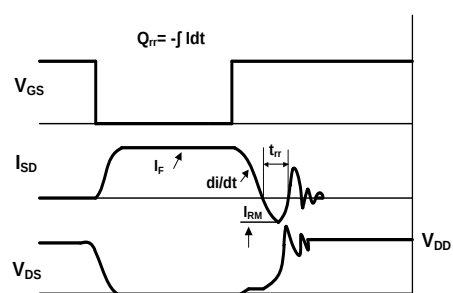
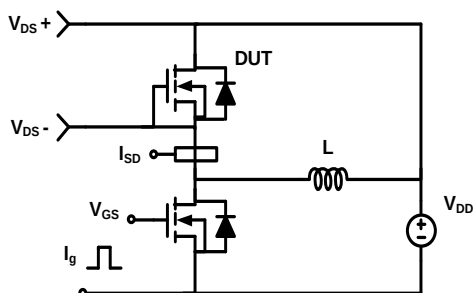
Resistive Switching Test Circuit & Waveform



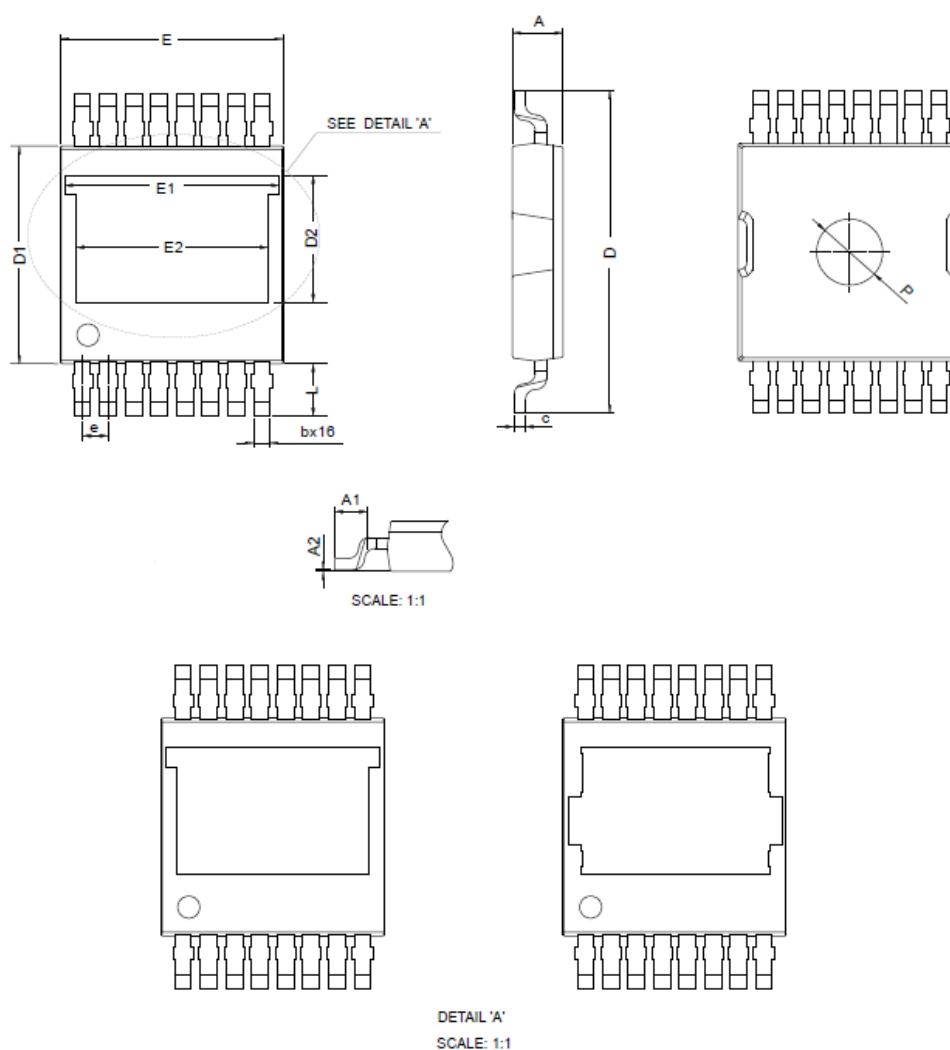
Unclamped Inductive Switching (UIS) Test Circuit & Waveform



Diode Recovery Test Circuit & Waveform



Mechanical Dimensions for TOLT



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	2.25	2.35	0.089	0.093
A1	1.40	1.60	0.055	0.063
A2	0.01	0.16	0.000	0.006
b	0.60	0.80	0.024	0.031
c	0.40	0.60	0.016	0.024
D	14.80	15.20	0.583	0.598
D1	10.00	10.30	0.394	0.406
D2	5.57	5.77	0.219	0.227
E	9.70	10.10	0.382	0.398
E1	8.70	9.46	0.343	0.372
E2	8.25	8.50	0.325	0.335
e	1.18	1.22	0.046	0.048
L	2.35	2.55	0.093	0.100
P	2.80	3.20	0.110	0.126

Version Information

LSTT65R099GF

Revision 1.1

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